

AMENDMENTS TO THE SPECIFICATION

Please replace lines 15-24 on page 13 with the following:

Fig. 6 is a graph illustrating the reverse link load in an alternate embodiment of the present invention. In Fig. 6, L_{MAX} is the maximum load beyond which the system is unstable and outages are likely to occur. L_{MIN} is the load below which the system is considered lightly loaded. L_T is a target load at which the RBS 36 should operate. The values L_{MAX} , L_T , and L_{MIN} divide the range of possible load values into four regions, which can be indicated by two bits. In this embodiment, the RBS 36 determines the load indication $b(n)$ as follows:

$$\begin{array}{ll}
\text{if} & (L(n) > L_{MAX}) \quad \{set\ b(n) = 2\} \\
\text{else if} & (L_T \geq L(n) \geq L_T) \quad \{set\ b(n) = 1\} \\
\text{else if} & (L_{MAX} \geq L(n) > L_T) \quad \{set\ b(n) = 1\} \\
\text{else if} & (L_T \geq L(n) > L_{MIN}) \quad \{set\ b(n) = -1\} \\
\text{else} & \{set\ b(n) = -2\}
\end{array}
\tag{Eq. 4}$$

The load indication $b(n)$ may comprise, for example, a pair of reverse activity bits (RABs) with the values shown in Fig. 6.